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Date: January 25, 2002
Docket No.: 0397-0439P

Assistant Commissioner for Patents
Box PATENT APPLICATION
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): MATSUMOTO, Hironori
OHMI, Toshinori

For: INTEGRATED SEMICONDUCTOR CIRCUIT DEVICE, PROCESS OF
MANUFACTURING THE SAME, IC MODULE AND IC CARD

Enclosed are:

- A specification consisting of 18 pages
- 6 sheet(s) of FORMAL drawings
- An assignment of the invention - \$40.00 Recording Fee
- Certified copy of Priority Document(s)
- Executed Declaration Original Photocopy
- Applicant claims small entity status in accordance with 37 CFR 1.27
- Application Data Sheet in accordance with 37 C.F.R. 1.76

Preliminary Amendment
 Information Disclosure Statement, PTO-1449 and reference(s)
 Other _____
 Applicant requests early publication

The filing fee has been calculated as shown below:

		LARGE ENTITY		SMALL ENTITY	
FOR	NO. FILED	NO. EXTRA	RATE FEE	RATE FEE	
BASIC FEE	***** ***** *****	***** ***** *****	***** ***** \$740.00 *****	or ***** **** \$370.00 ***	
TOTAL CLAIMS	10 - 20 =	0	x18 = \$ 0.00	or x 9 = \$ 0.00	
INDEPENDENT	2 - 3 =	0	x84 = \$ 0.00	or x 42 = \$ 0.00	
MULTIPLE DEPENDENT CLAIM PRESENTED	no		+280 = \$ 0.00	or +140 = \$ 0.00	
			TOTAL \$ 740.00	TOTAL \$ 0.00	

A check in the amount of \$ 780.00 to cover the filing fee and recording fee (if applicable) is enclosed.
 Please charge Deposit Account No. 02-2448 in the amount of \$ _____. A triplicate copy of this transmittal form is enclosed.
 No fee is enclosed.

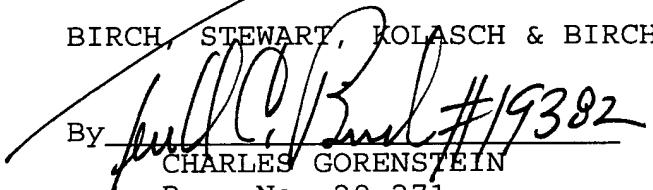
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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 

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